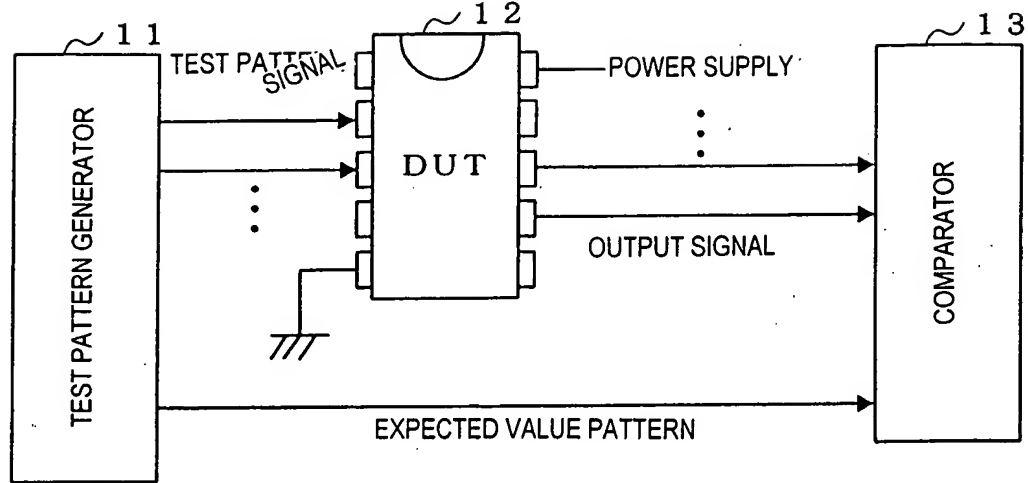


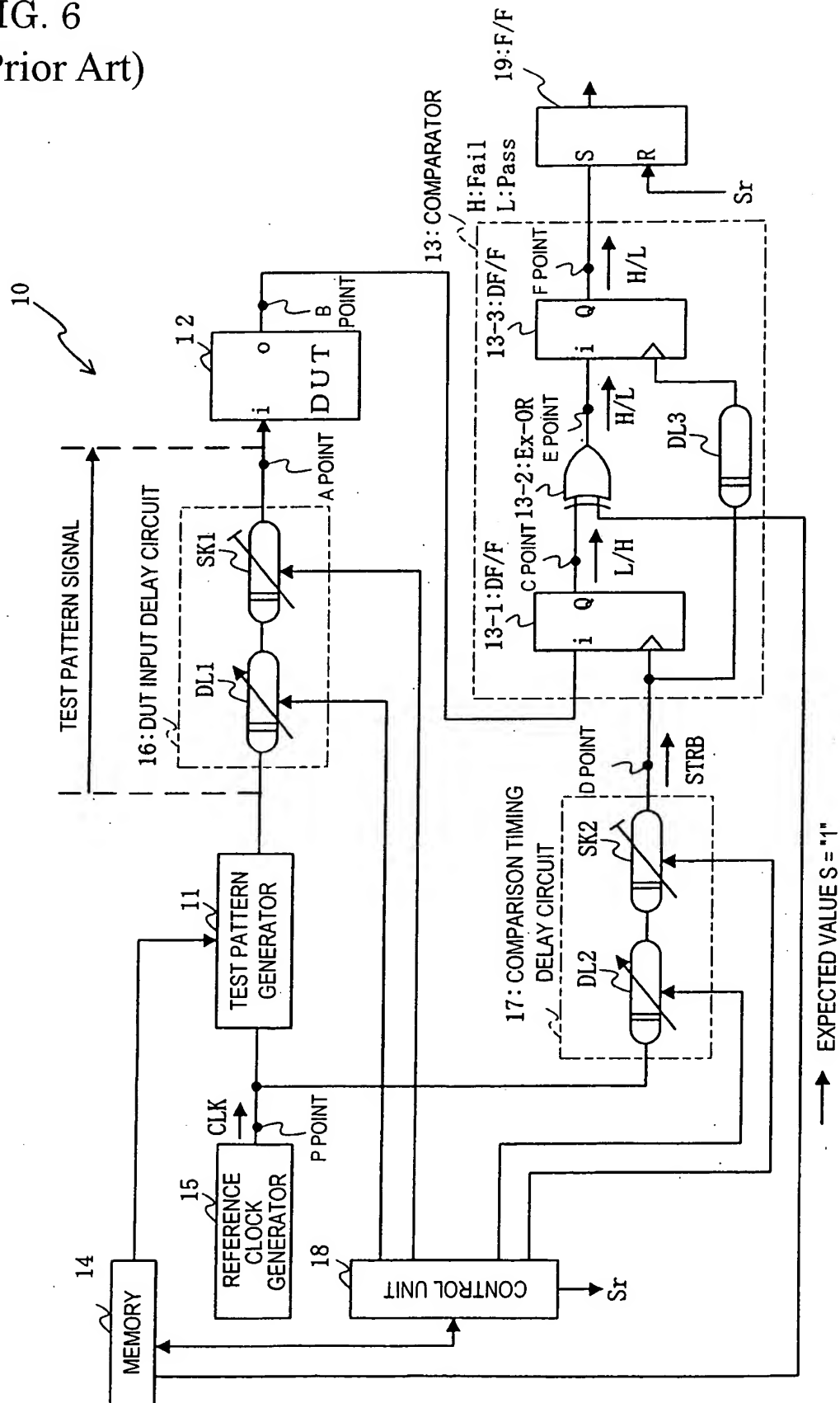
5/10

FIG. 5
(Prior Art)

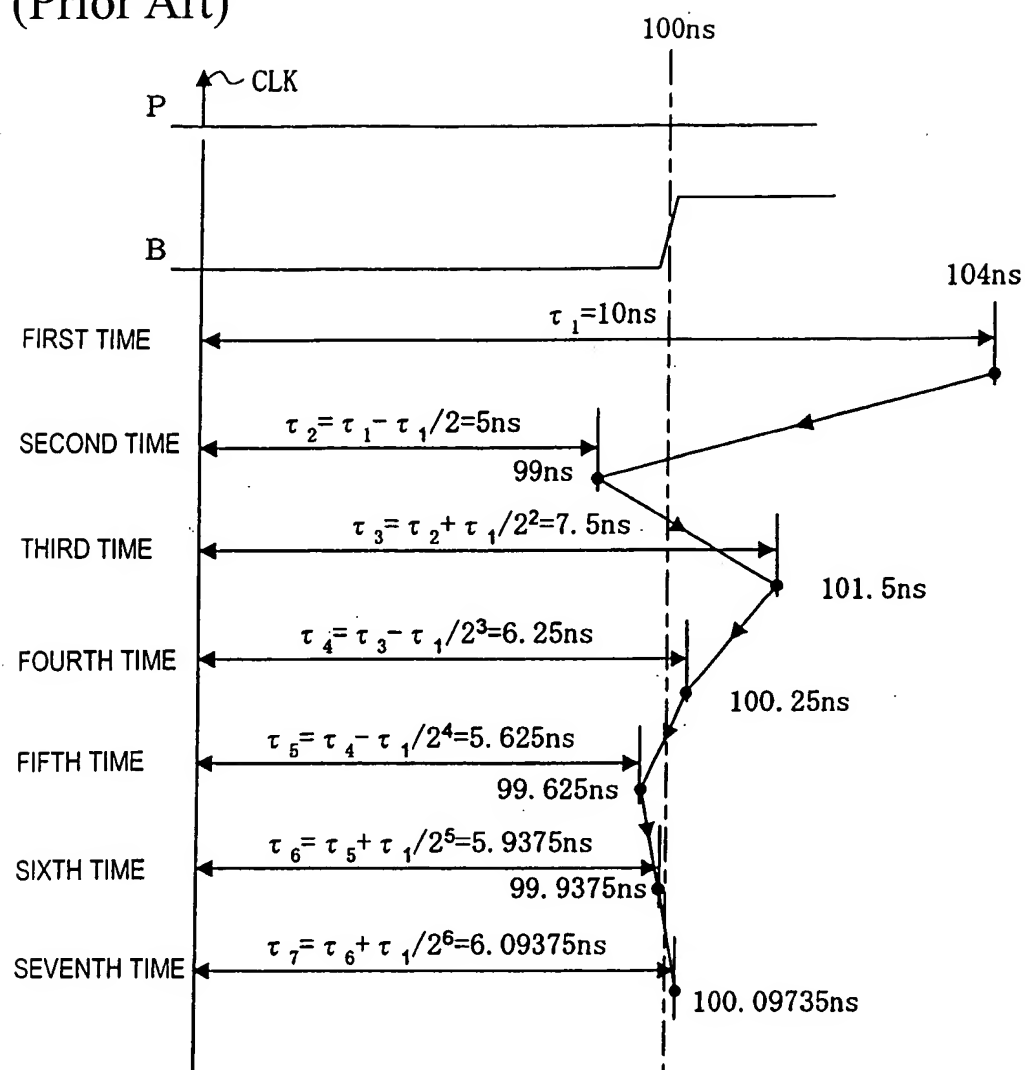


10 : SEMICONDUCTOR TEST APPARATUS

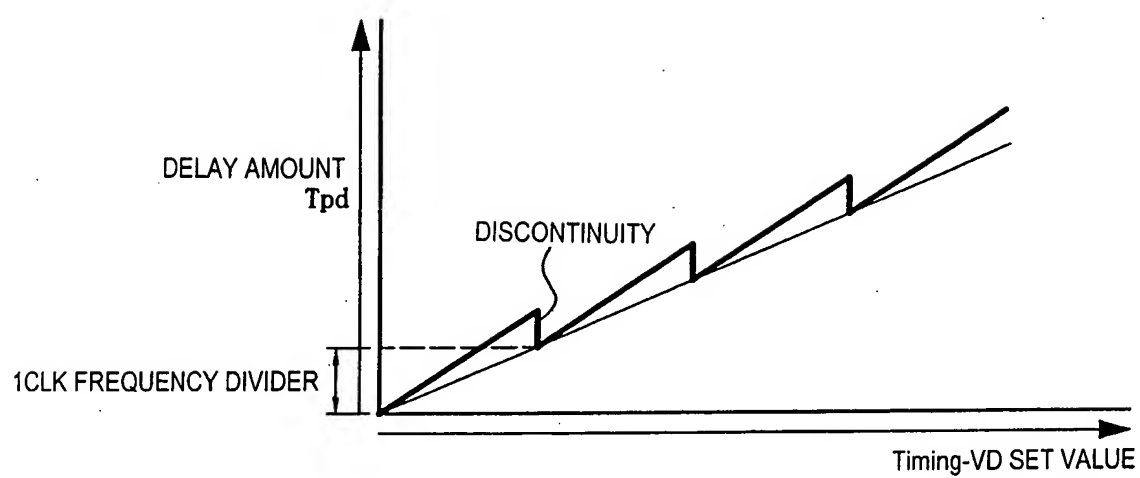
6/10

FIG. 6
(Prior Art)

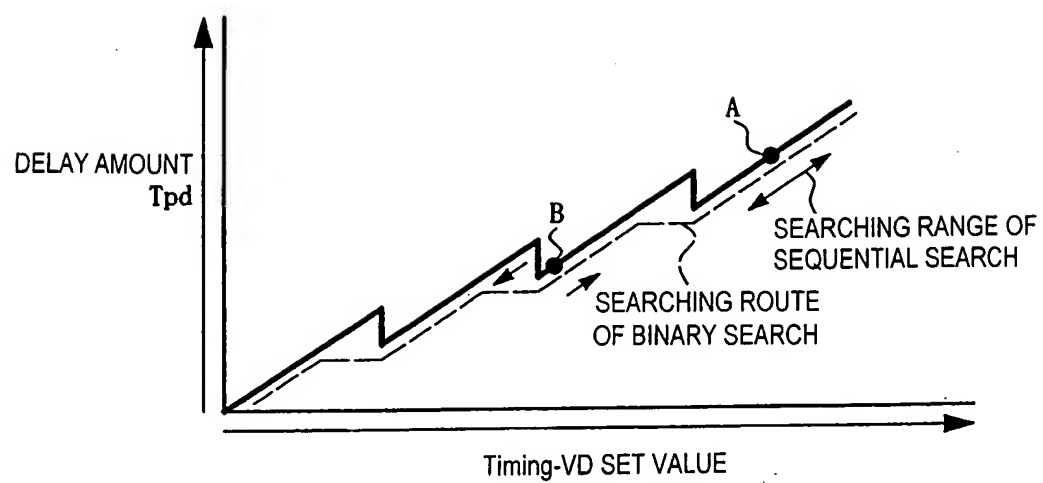
7/10

FIG. 7
(Prior Art)

8/10

FIG. 8
(Prior Art)

9/10

FIG. 9
(Prior Art)

10/10

FIG. 10
(Prior Art)